

FIG. 1A

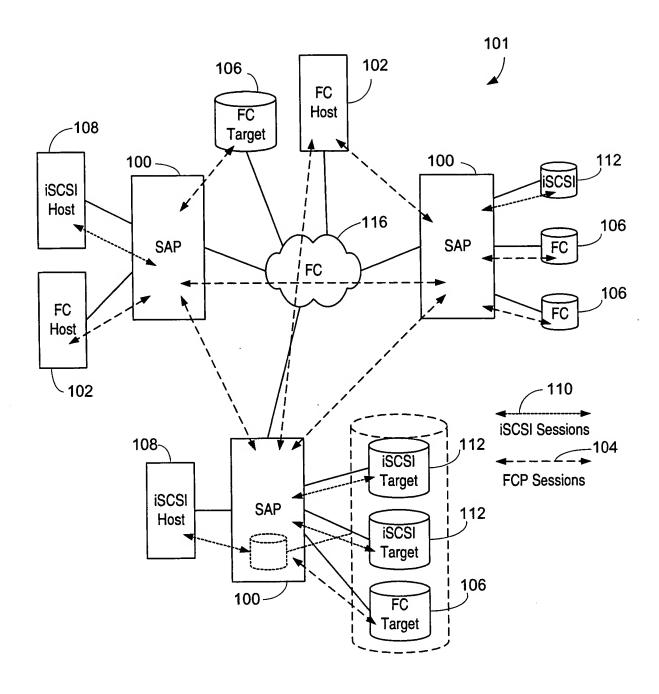


FIG. 1B

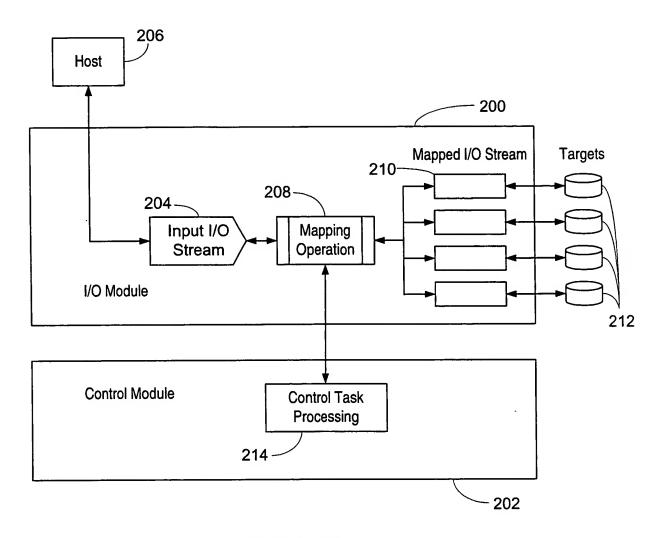


fig. 2

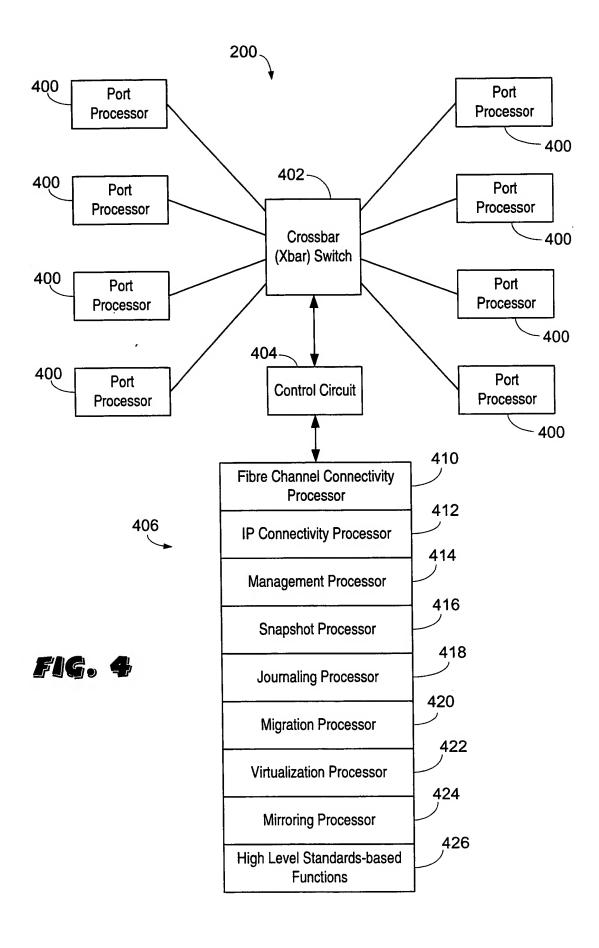
Fibre Channel Connectivity	Processor	IP Connectivity Processor	Management Processor	
Industry APIs	Topology & Discovery Routines	Network Management		
Software				

Connectivity

Custom Application Segments Virtualization Engine I/O Processor Port Processors Firmware

Custom Application Segments Crossbar Switch Silicon





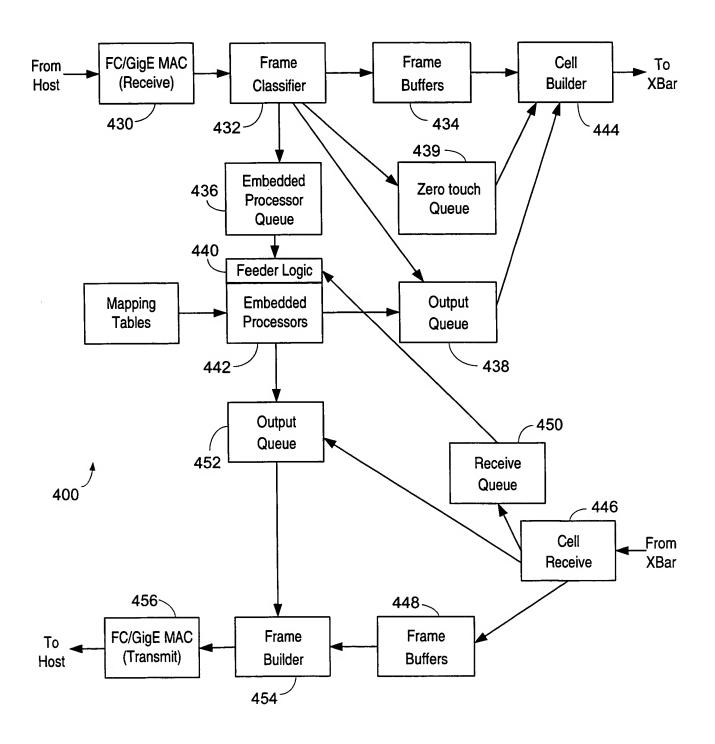


fig. 5

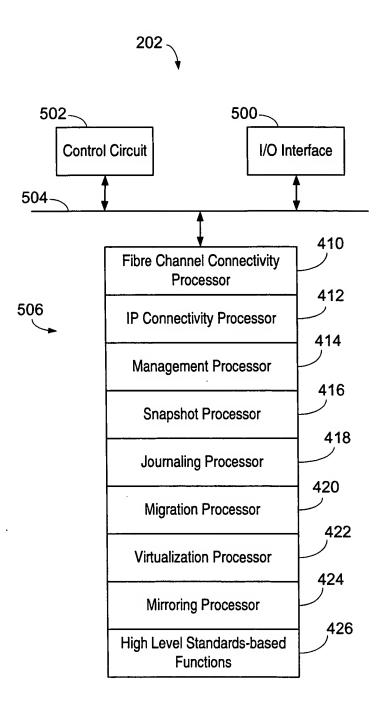


FIG. 6

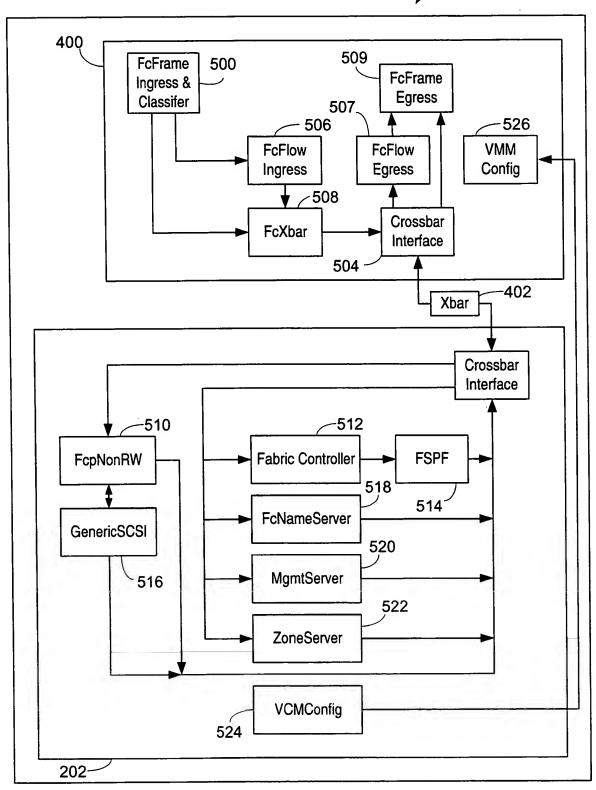


fig. I

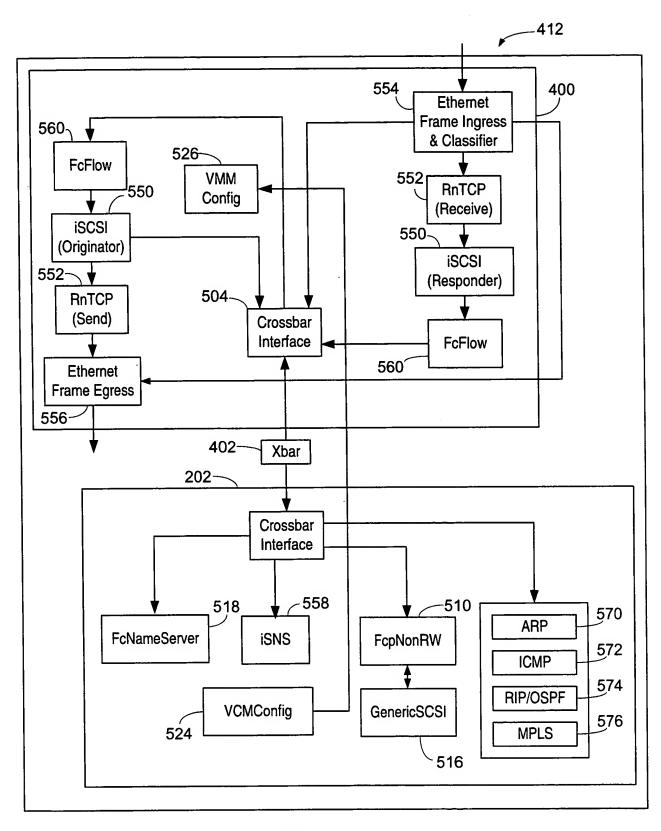
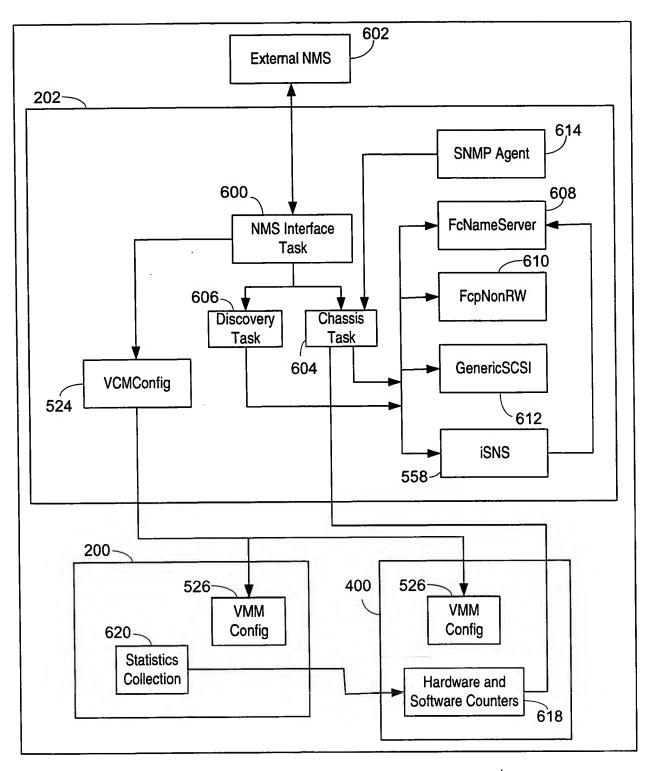


fig. 8



<u>_414</u>

fig. 9

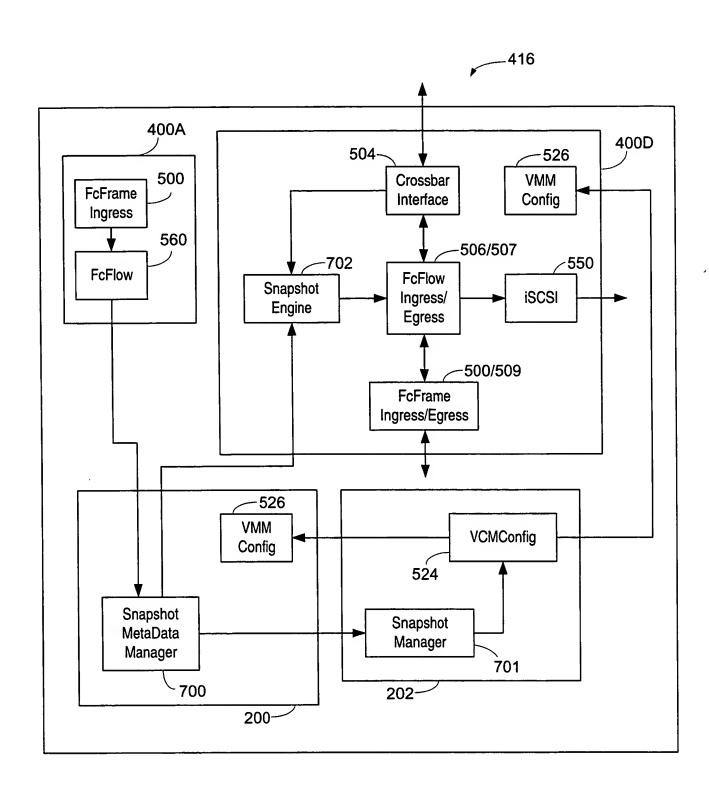


FIG. 10

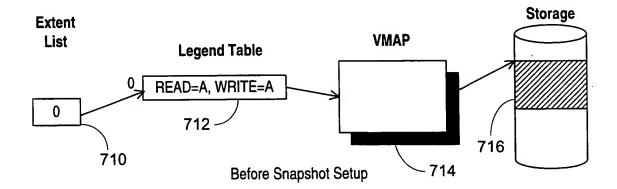


FIG. 11

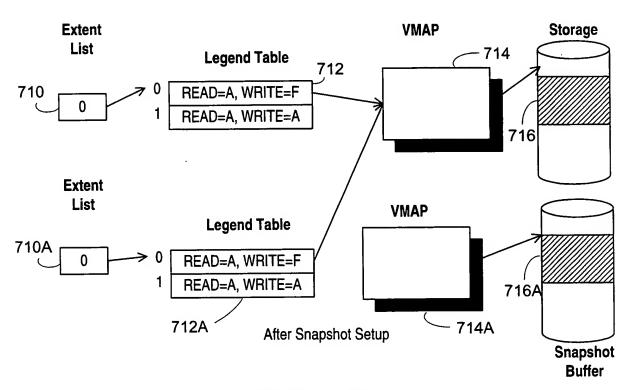


FIG. 12

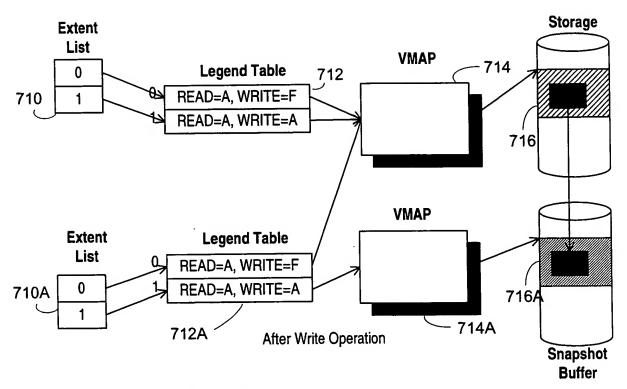
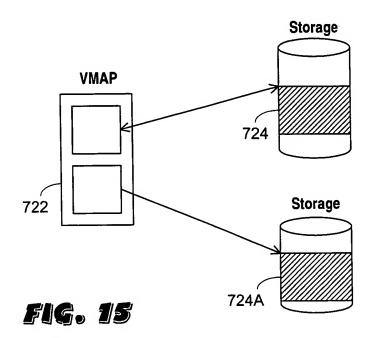
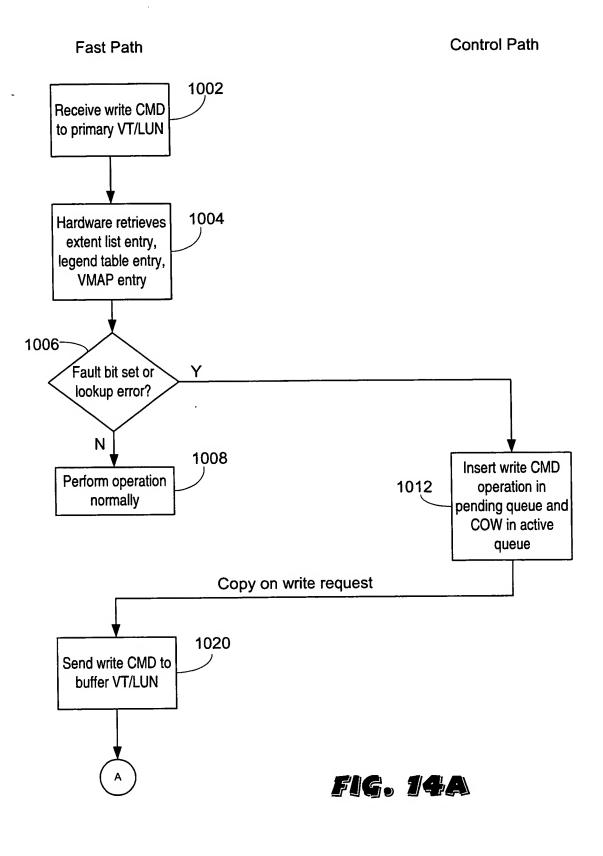


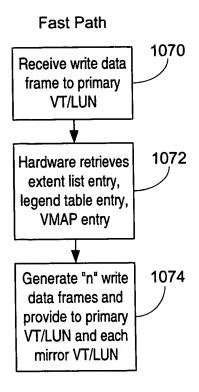
FIG. 13

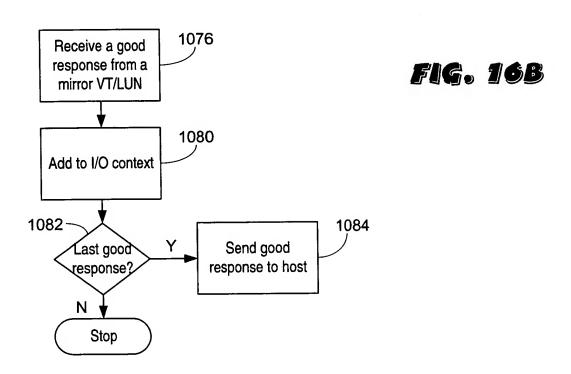


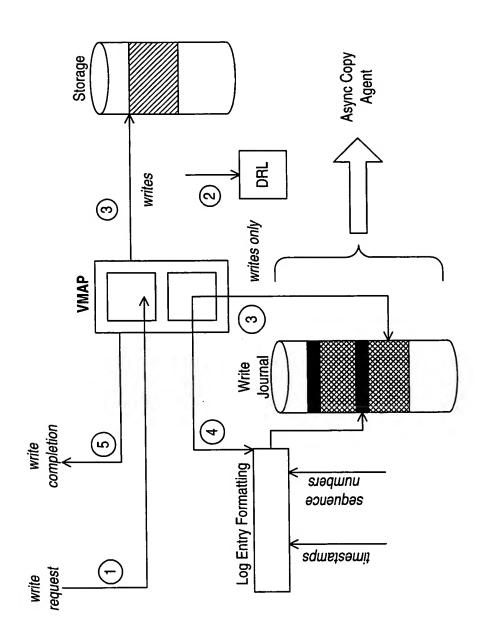


Fast Path

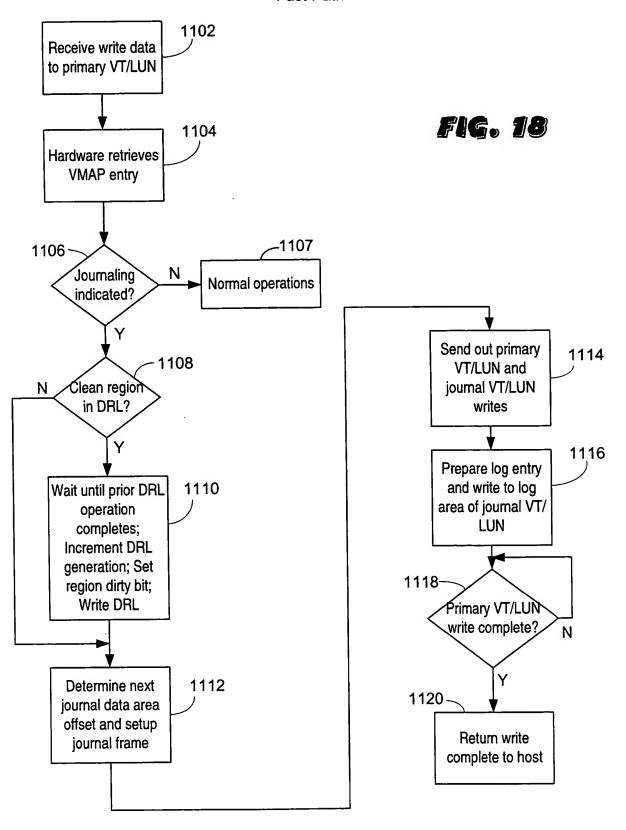
Control Path







Fast Path



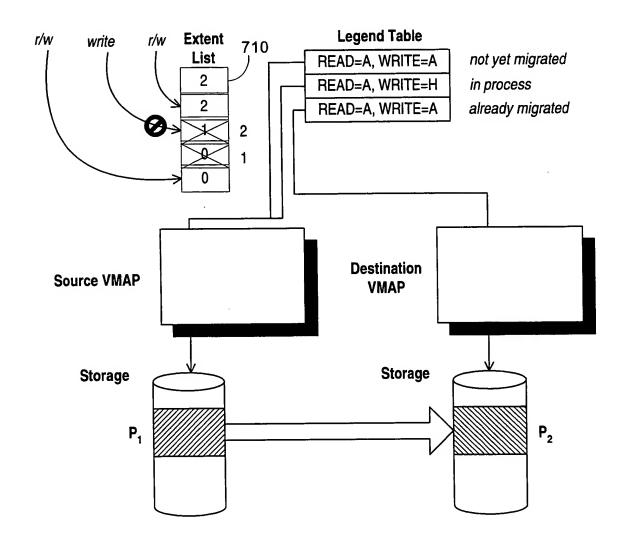


FIG. 19

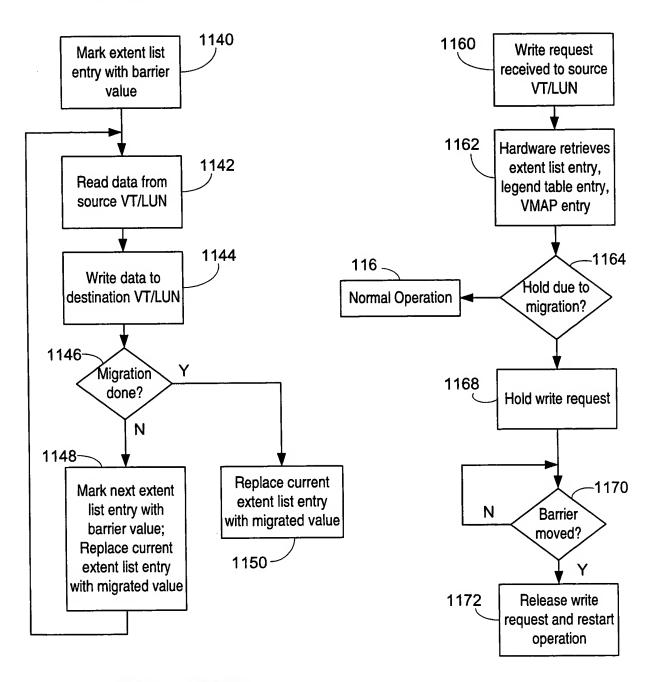
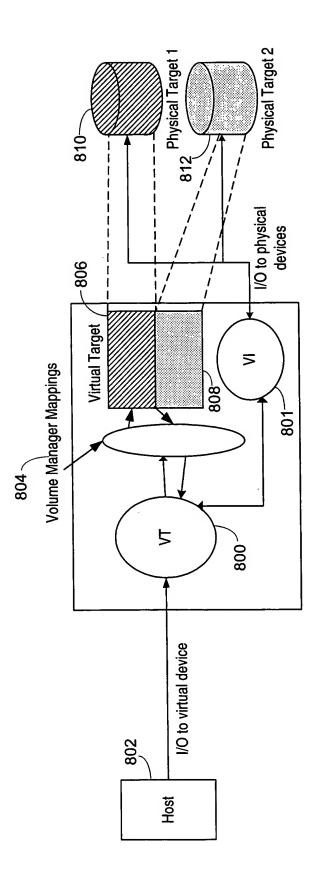
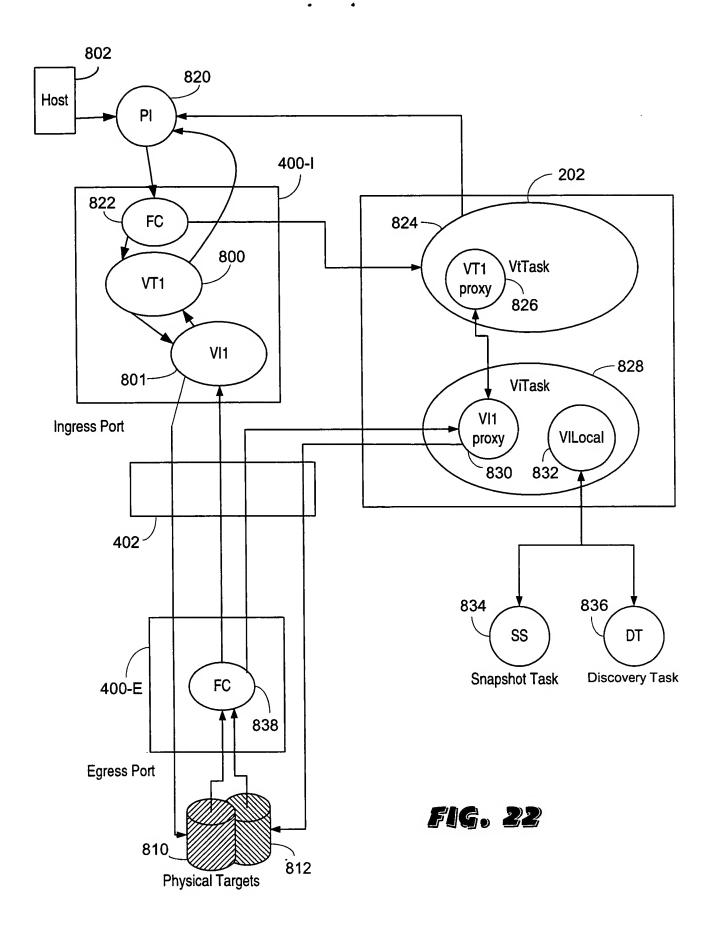


FIG. 20A

FIG. 20B





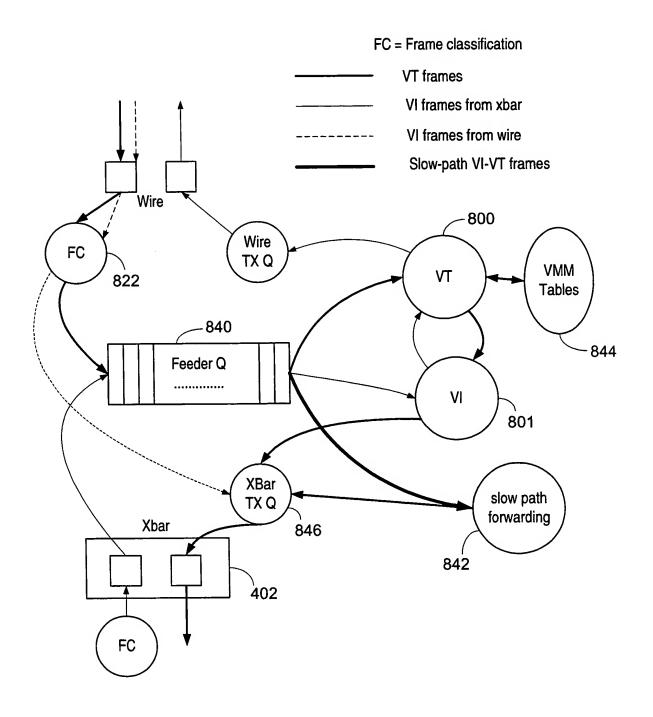
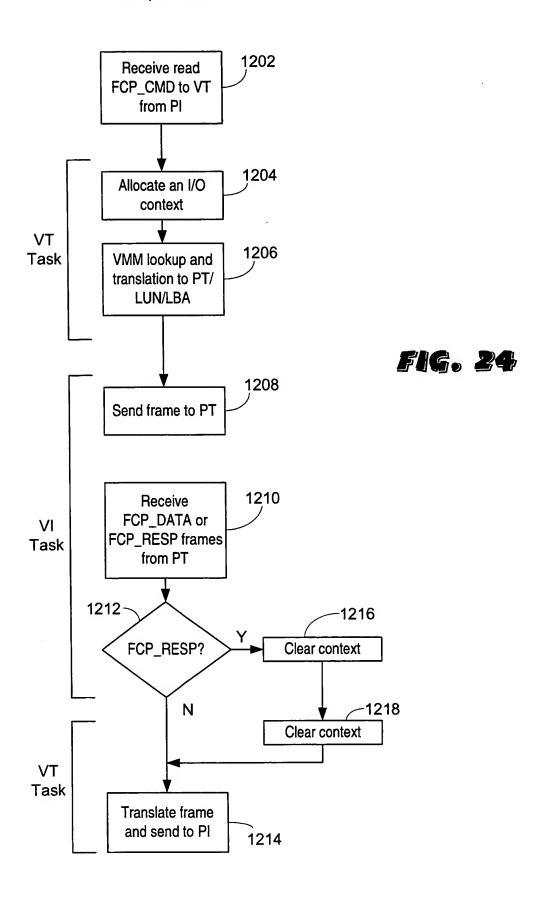


FIG. 23



Simple Write

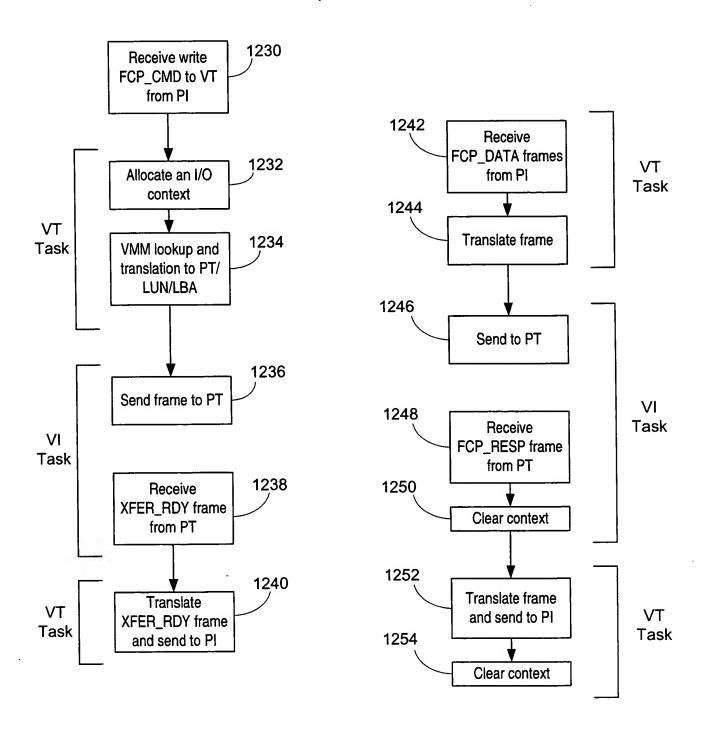


FIG. 25

Spanned Read

